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2819  
#10/Amend C  
9/3/02  
XA-8472

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Yasuhisa SHIMAZAKI et al.

Appln. No.: 09/855,660

Group Art Unit: 2819

Filed: May 16, 2001

Examiner: D. Chang

For: SEMICONDUCTOR INTEGRATED CIRCUIT

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AMENDMENT

Commissioner for Patents  
Washington D. C. 20231

Sir:

In response to the Office Action mailed May 10, 2002,  
please amend the above-identified patent application as  
indicated below.

IN THE TITLE:

Please change the title of the invention to:

--SEMICONDUCTOR INTEGRATED CIRCUIT HAVING HIGH-SPEED AND  
LOW-POWER LOGIC GATES WITH COMMON TRANSISTOR SUBSTRATE  
POTENTIALS, AND DESIGN DATA RECORDING MEDIUM THEREFOR--.

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